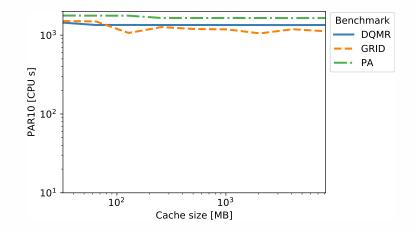
# Caching in Model Counters: A Journey through Space and Time

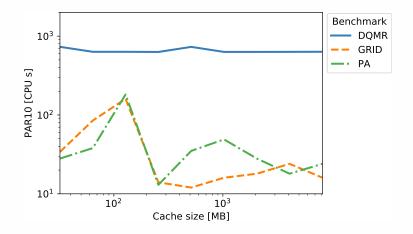
Jeroen Rook, Anna Latour, Holger H. Hoos and Siegfried Nijssen.



## Effect of cache size limit

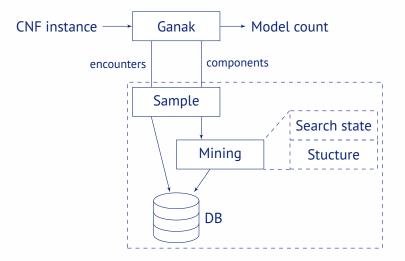


## Effect of cache size limit



What is the effect of branching heuristics and cache management schemes on caching behaviour?





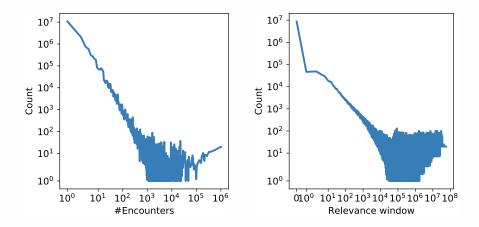
## **Relevance measures**



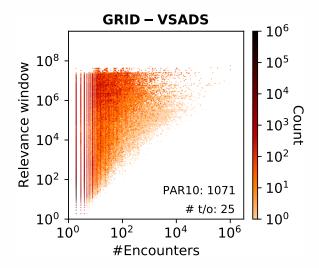
**#Encounters** 

#### **Relevance window**

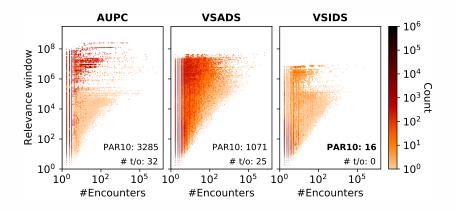
### **Relevance measure distributions**



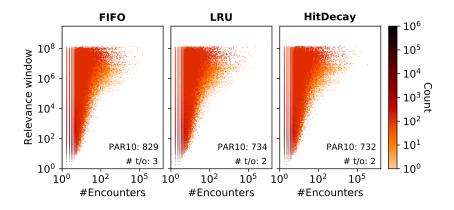
### **Relevance measure heatmaps**



## Caching behaviour for branching heuristics



## Caching behaviour for caching schemes



## **Future work**

- Caching behaviour under symmetric component caching [vBDS<sup>+</sup>21]
- New caching schemes based on caching behaviour.
  - Based on component characteristics.
  - Adaptive thresholds

## Take aways

- Trade-off between space and time in DPLL-style model counters is small.
- Good branching heuristics yield small search trees and shortly relevant components.
- How low can we go?

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## **References I**

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